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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicants:</b>	Brian Messenger et al.	<b>Docket:</b>	FIS920040004US1 (17368)
<b>Serial No.:</b>	10/710,738	<b>Examiner:</b>	Mursalin B. Hafiz
<b>Filed:</b>	July 30, 2004	<b>Art Unit:</b>	2814
<b>For:</b>	MANUFACTURABLE RECESSED STRAINED RSD STRUCTURE AND PROCESS FOR ADVANCED CMOS	<b>Dated:</b>	January 9, 2006
		<b>Confirmation No:</b>	4737

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE UNDER 37 C.F.R. § 1.111**

Sir:

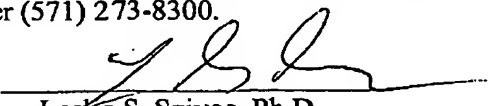
In response to the Office Action dated October 13, 2005, Applicants hereby submit the following remarks for entry into the record of the above-identified patent application.

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**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this document is being filed in the United States Patent and Trademark Office on the date shown below via facsimile transmission to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 at United States Patent and Trademark Office facsimile transmission number (571) 273-8300.

Dated: January 9, 2005

  
Leslie S. Szivos, Ph.D.

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